IDEC Chip Design Contest



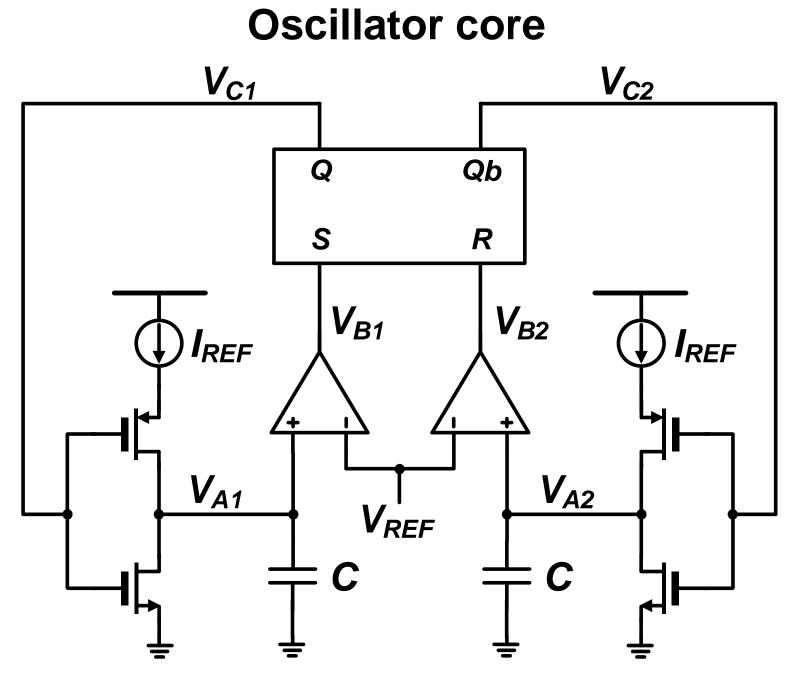
2021 IDEC Congress CDC

RC Relaxation Oscillator for Frequency Reference

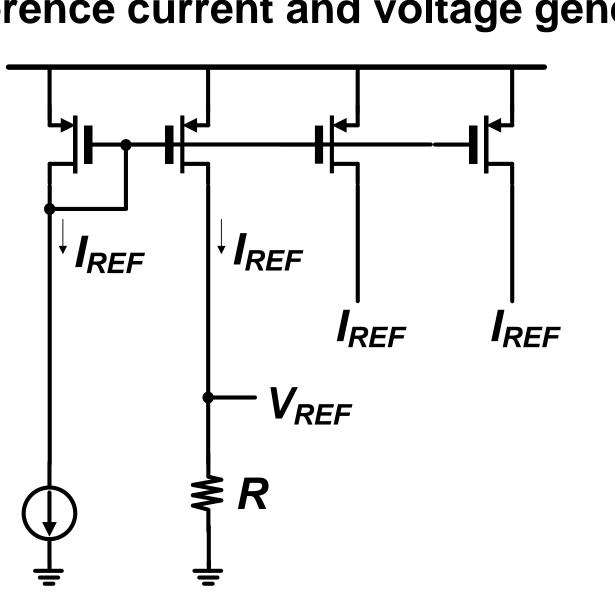
Dongmin Lee and Byong-Deok Choi

Department of Electronic Engineering, Hanyang University, Seoul, Korea

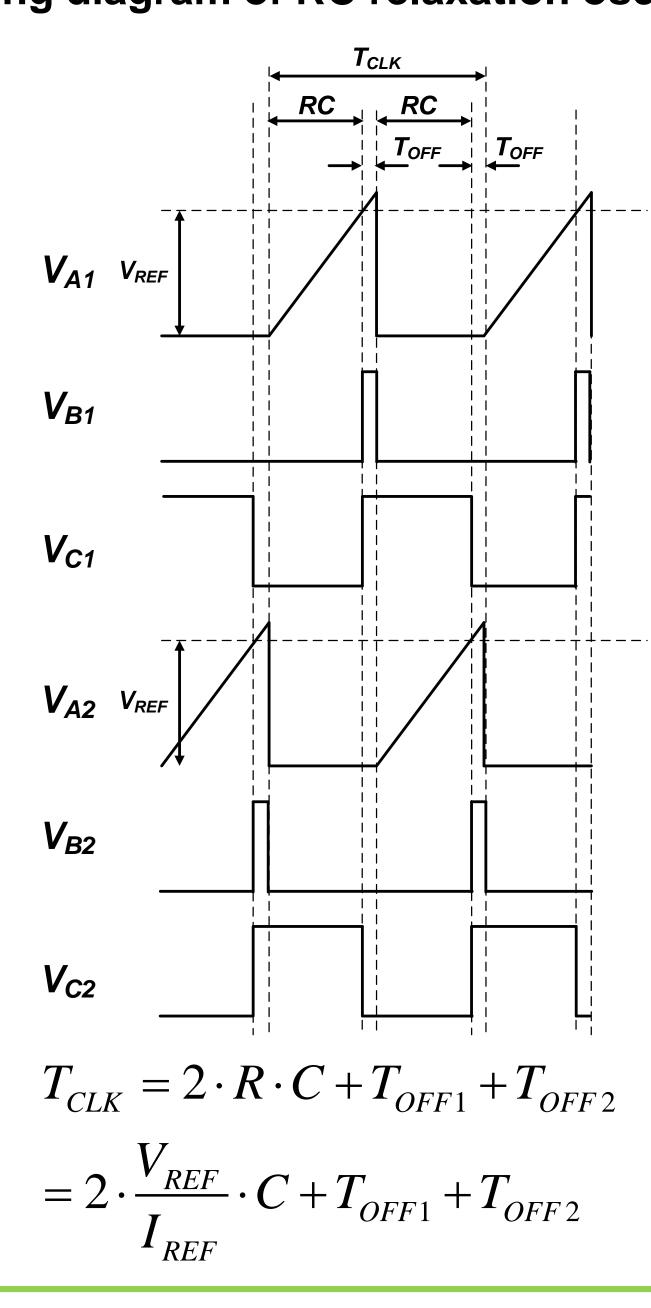
Structure of RC relaxation oscillator



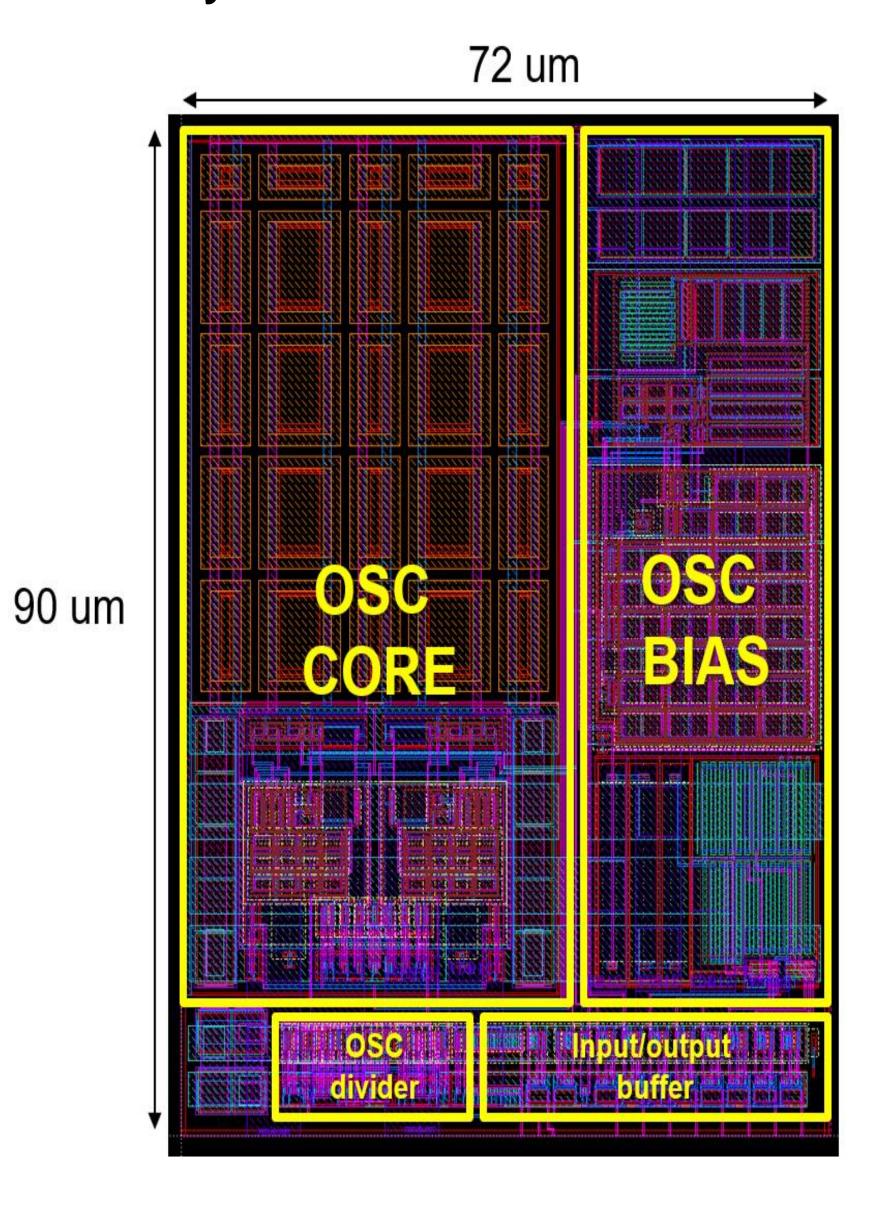
Reference current and voltage generator



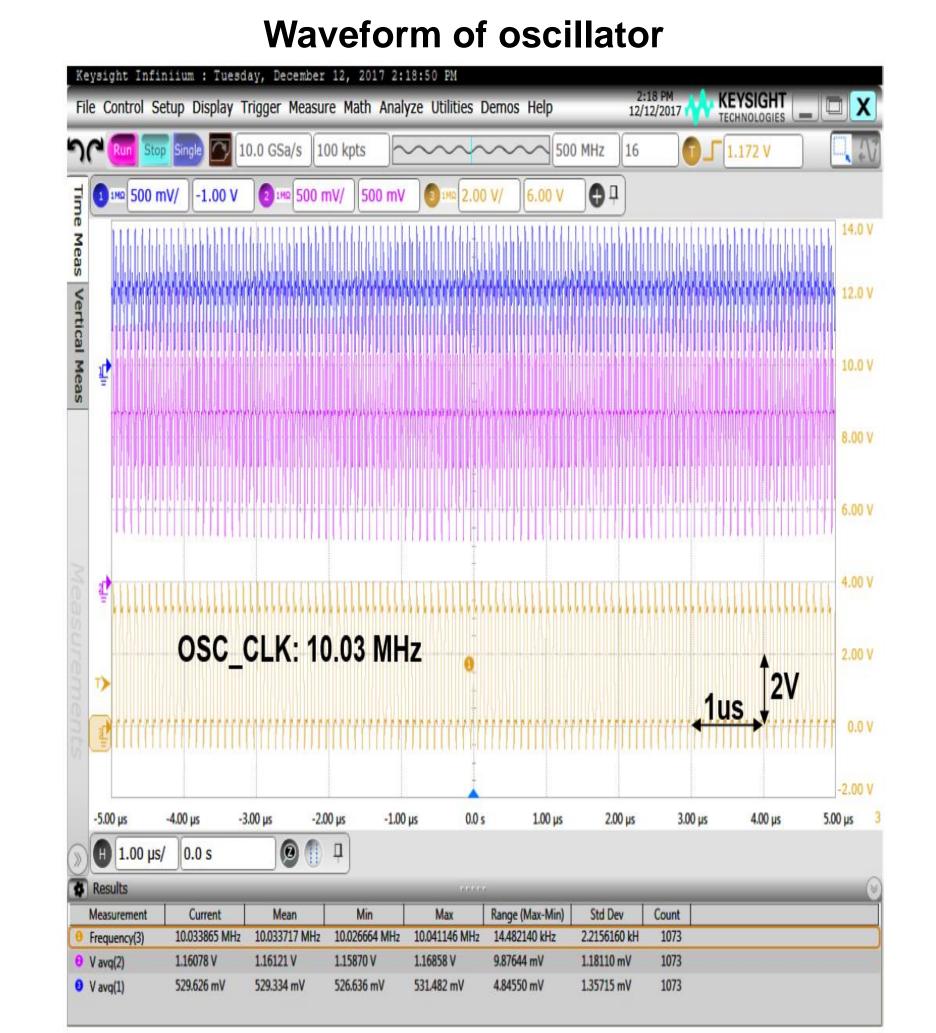
Timing diagram of RC relaxation oscillator



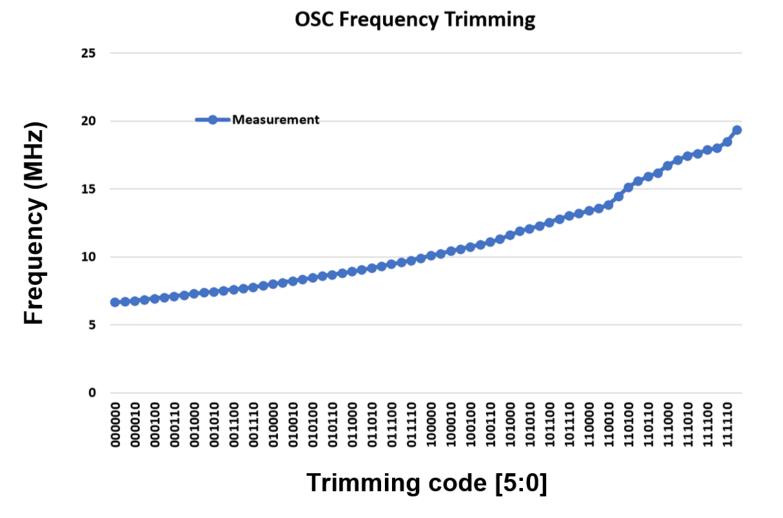
Layout of RC relaxation oscillator



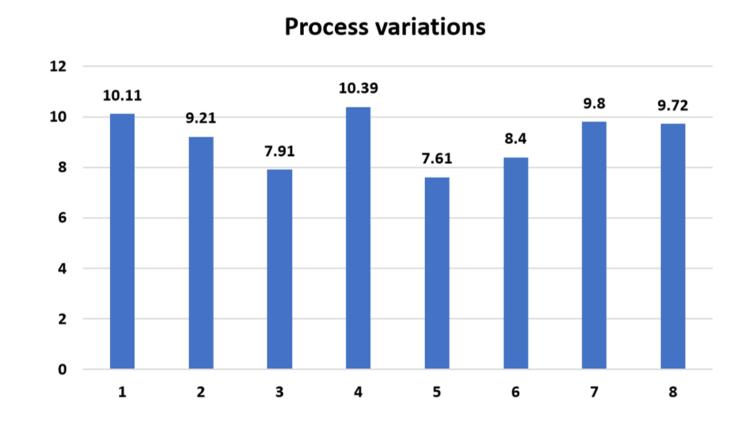
Measurement result



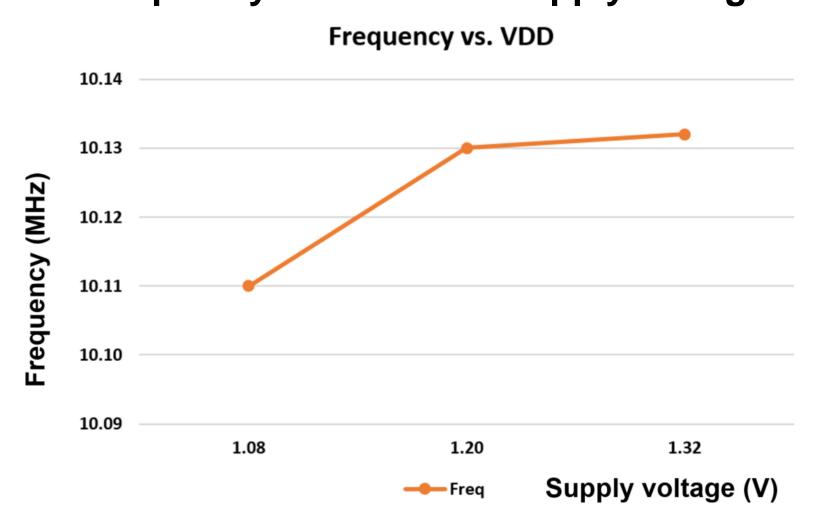
Oscillator frequency along with trimming code

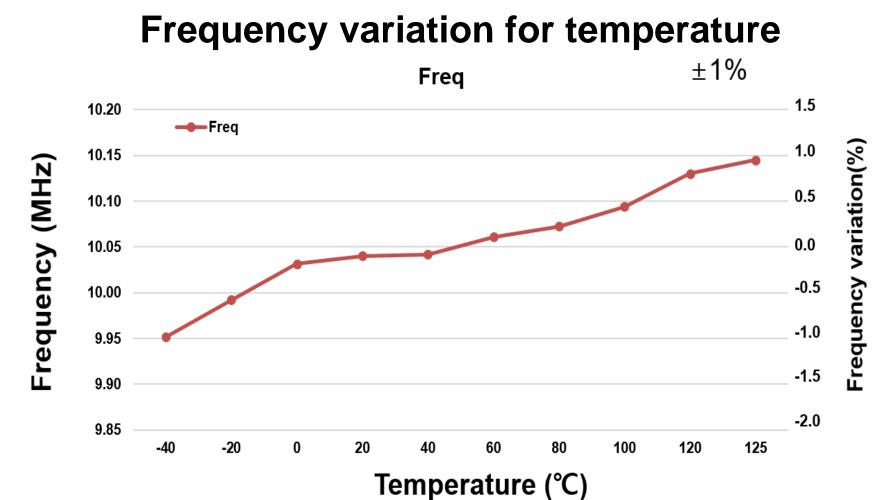


Oscillator frequency for process variation



Frequency variation for supply voltage





Symbol	ltem	Condition	Specification			Post-simulation			Measurement			Unit	Note
			Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Oille	NOLE
F _{CLK}	Clock Frequency	Monte-Carlo with 3-sigma	9.5	10	10.5	9.91	10	10.13	7.61	9.14	10.39	MHz	Spec: Typ. ±5% Meas.: ±14%
ΔF_{T}	Temperature accuracy	1.8V, -40~125°C	9.5	10	10.5	9.96 (-0.25%)	9.98	10.01 (+0.25%)	57 5767	10.13	10.15	MHz	Spec: Typ. ±5% Meas.: ±0.99%
ΔF_{V}	Voltage accuracy	1.62~1.98V, 25°C	9.5	10	10.5	9.88 (-1.2%)	9.98	10.02	10.11	10.13	10.13	MHz	Spec: Typ. ±5% Meas.: ±0.1%

* IDEC 지원